

## DEPARTMENT OF ELECTRICAL AND COMPUTER ENGINEERING

ECE 27000 - Introduction to Digital System Design
Required for CmpE and EE Programs
An introduction to digital system design and hardware engineering, with an emphasis on practical design techniques and circuit implementation.
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Class: 3, Lab: 3
ENGR 12800
Basic understanding of circuits (voltage, current, Ohm's Law) and electrical components (resistors, capacitors, switches).
W. Kleitz, <i>Digital Electronics - A Practical Approach</i> , Prentice Hall, Current Edition.
This course will provide a comprehensive understanding of the principles and practices of digital logic circuits. Students should be able to analyze, design and implement combinational and sequential circuits.
The laboratory sessions form an integral part to provide practical experiences in hardware and software analysis and design.
<ol> <li>On successful completion of this course, students should be able to:         <ol> <li>Understand the basic characteristics of integrated-circuit logic devices (1).</li> <li>Analyze and simply combinational logic circuits (1).</li> <li>Design combinational logic circuits with building blocks such as adders, encoders, decoders, multiplexers and demultiplexers (2).</li> <li>Understand the operations of flip-flops and use timing waveforms to analyze sequential logic circuits (1).</li> <li>Design sequential logic circuits(2).</li> <li>Realize, test, and debug practical digital circuits (6).</li> <li>Use programmable logic devices and computer aided design (CAD) tools to design digital circuits (7).</li> </ol> </li> </ol>

	8. Present lab results effectively in forms of lab reports (3).
Lecture Topics	<ol> <li>Number systems and codes</li> <li>Digital electronic signals and switches</li> <li>Basic logic gates</li> <li>Programmable logic devices</li> <li>Boolean algebra and reduction techniques</li> <li>Timing hazards</li> <li>Exclusive-OR and Exclusive-NOR gates</li> <li>Arithmetic operations and circuits</li> <li>Code converters, multiplexers, and demultiplexers</li> <li>Logic families and their characteristics</li> <li>Flip-flops and registers</li> <li>Practical considerations for digital design</li> <li>Counter circuits</li> <li>Shift registers</li> <li>Synchronous sequential state machines</li> </ol>
Computer Usage	High
Laboratory Experience	High
Design Experience	High
Coordinator	Chao Chen, Ph.D.
Date	09/15/2018